Attorney Docket No. 0756-2294

IN THE ONTED STATES P.	MIENIA	AND TRADEMARK OFFICE	CENTRAL FAX CENTE
n re Patent Application of:)	Group Art Unit: 2822	AUG 1 6 2004
Shunpei YAMAZAKI et al.)	Examiner: M. Lewis	OFFICIAL 2004
Serial No. 09/832,867)	CERTIFICATE OF MAILING I hereby certify that this correspondence is be	UFFICIAI
Filed: April 12, 2001)	deposited with the United States Postal Serv	rice
For: LIGHT EMITTING DEVICE)	with sufficient postage as First Class Mail in envelope addressed to: Commissioner for Pater	ntş,
COMPRISING THIN FILM)	P.O. Box 1450, Alexandria, VA 22313-1450,	ол .

INFORMATION DISCLOSURE STATEMENT

IN THE UNITED STATES DATENT AND TRADEALA

Honorable Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

TRANSISTOR WITH DISTINCT

AND IMPURITY REGIONS

POSITION OF GATE ELECTRODE

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The Examiner's attention is directed to U.S. Patent Application Nos. 09/454,146 and 10/386,257 which are related to the present patent application.

A check in the amount of \$180 is being submitted to comply with the provisions of 37 C.F.R. § 1.97(c).

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280.

NO. 3672 AUG 1 6 2004

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PMB 955 21010 Southbank Street Potomac Falls, Virginia 20165

August 16, 2004

571-434-6789 (Voice) 571-434-9499 (G3 Facsimile) erobinson@riplo.com www.riplo.com

FACSIMILE COVER SHEET

To: Examiner Lev	<i>i</i> s	From:	Eric J.	Robinson
Facsimile Number:	571-273-1838 703-872-9306	Teleph	one No.	571-272-1838
Number of Pages (in	ncluding cover sheet):			
RE: Serial No. 09/8 Our Reference				

Examiner Lewis:

As you requested, attached please find a copy of the IDS and Form PTO-1449 filed on December 8, 2003, along with the stamped receipt card evidencing receipt from the Patent Office.

Eric Robinson Reg. No. 38,285 This will acknowledge receipt of the following:

- Information Disclosure Statement
- 2. PTO Form-1449 with copies of references listed thereon
- Check No. 2640 in the amount of \$180.00 (ids)
- Certificates of Mailing for each of the above 3.

In re PATENT application of:

SHUNPEI YAMAZAKI et al Serial No. 09/832,867 Filed: 04/12/2001

For: LIGHT EMITTING DEVICE COMPRISING THIN FILM TRANSISTOR WITH DISTINCT Due Date: 12/01/2003

POSITION OF GATE ELECTRODE AND IMPURITY REGIONS

Docket: 0756-2294 12. 8.03

EJR/EJR/ams

- 2 - Application Serial No. 09/832,867 Attorney Docket No. 0756-2294

A duplicate copy of this sheet is attached.

Respectfully submitted,

Eric J. Robinson Reg. No. 38,285

Robinson Intellectual Property Law Office, P.C. PMB 955 21010 Southbank Street Potomac Falls, Virginia 20165 (571) 434-6789

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PTO/SB/08A (08-00)

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Substitute	Substitute for form 1449A/PTQ			Complete if Known		
INFO	DMATION I	חופרו	TOSTIBE	Application Number	09/832,867	
	INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use at many sheets as necessary)			Flling Date	April 12, 2001	
SIAI				First Named Inventor	Shunpei YAMAZAKI, et al.	
				Group Art Unit	2822	
:			Examiner Name	Monica Lewis		
Sheet	l	of	3	Attorney Docket Number	0756-2294	

			U.S. PATENT DOCUMEN	T\$	
Examiner Initials	Cits No. ¹	U.S. Patoni Documeni Number Kind Code ³	Name of Patemate or Applicant of Cited Opcurrent	Date of Publication of Cited Decument MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	<u>.</u>	(If known)			
		4,963,504	Huang	10/16/90	
		5,323,042	Matsumoto	06/21/94	
		5,399,502	Friend et al.	03/21/95	
		5,412,240	Inoue et al.	05/02/95	
		5,532,175	Racanelli et al.	07/02/96	
		5,543,340	Lee	08/06/96	
·	T -	5,543,947	Mase et al.	08/06/96	
		5,583,067	Sanchez	12/10/96	
		5,581,092	Takemura	12/03/96	
		5,585,295	Wu	12/17/96	
	<u> </u>	5,594,569	Konuma et al.	01/14/97	
		5,637,519	Tsai et al.	06/10/97	
	1	5,686,328	Zhang et al.	11/11/97	
-	1	5,767,930	Kobayashi et al.	06/16/98	
		5,757,444	Takemura	05/26/98	
	†	5,841,170	Adan et al.	11/24/98	
		5,851,861	Suzawa et al.	12/22/98	
		5,852,488	Takemura	12/22/98	
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		5,903,249	Koyama et al.	05/11/99	
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	1.	6,166,414	Miyazaki et al.	12/26/00	
	+	6,198,133	Yamazaki et al.	03/06/01	
		6,211,536	Zhang	04/03/01	
	1	6,259,138	Ohtani et al.	07/10/01	
,	 	6,274,400	Jen	08/14/01	1
		6,274,887	Yamazaki et al.	08/14/01	
	+	6,277,679	Ohtani	08/21/01	

Examiner	Date	
Signature	Considered	

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation If not in conformance and not considered. Include copy of this form with next communication to applicant.

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Unique citation designation number. 2 See attached Kinds of U.S. Patent Documents. 2 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

¹Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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Substitut	Substitute for form 1449A/PTO			Complete if Known		
INFO	RMATION	DISC	LOSURE	Application Number	09/832,867	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE OS MARTY Sheets OF MECESSORY)				Filing Date	April 12, 2001	
				First Named Loventor	Shunpei YAMAZAKI, et al.	
				Group Art Unit	2822	
		Examiner Name	Monica Lewis			
Sheet	2	of	3	Attorney Docket Number	0756-2294	

				U.S. PATENT DOCUMEN	TS	
Examiner Cite Initials No.3		U.S. Patent Document		Name of Patentee or Applicant of Cited	Date of Publimation of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages of Relevant Figures Appear
		Number	Kind Code ² (If known)	Décument	NIM-DD-TYYY	
		6,281,552		Kawasaki et al.	08/28/01	
	1.	6,284,577		Suzawa et al.	09/04/01	
		6,303,963		Ohtani et al.	10/16/01	
		6,306,694		Yamazaki et al.	10/23/01	
		6,337,731	ı	Takemura	01/08/02	
	T	6,346,730		Kitakado et al.	02/12/02	
		6,358,766		Kasahara	03/19/02	
		6,362,507		Ogawa et al.	03/26/02	
	1	6,365,917		Yamazaki	04/02/02	
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		6,392,628		Yamazaki et al.	05/21/02	
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	_	6,407,430		Ohtani et al.	06/18/02	
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		6,469,317		Yamazaki et al.	10/22/02	

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS							
Examiner Cite No.							
		US Patent Application Serial No. 10/386,257 including specification, drawings, claims, and filing receipt; filed March 11, 2003; Inventor: Hideto Ohnuma.					
		US Patent Application Serial No. 09/454,146 including specification, drawings, claims, and filing receipt; filed December 3, 1999; Inventors: Shunpei YAMAZAKI, et al.					
•		Yoshida et al., "A Full-Color Thresholdless Antiferroelectric LCD Exhibiting Wide Viewing Angle with Fast Response Time," SID 97 DIGEST, pp. 841-844.	ENG				
		Hwang et al., "High Performance Submicron SOI/CMOS with an Elevated Source/Drain Structure," PROCEEDINGS OF THE ANNUAL SOS/SOI TECHNOLOGY CONFERENCE, IEEE, Vol. 19, 1993, pp. 132-133.	ENG				
		Choi et al., "Gate-Overlapped Lightly Doped Drain Poly-Si Thin-Film Transistors for Large Area-AMLCD," IEEE TRANSACTIONS ON ELECTRON DEVICES, Vol. 45, No. 6, June, 1998, pp. 1272-1279.	ENG				

Examiner	Date	
Signature	Considered	

[•]EXAMINÉR: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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U.S. Petent and Trademark Office:

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INFO	RMATION I	ነነፍሮ	LOSURE	Application Number	09/832,867
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	(use as many sheets	as necessa	(קיי	Group Art Unit	2822
				Examiner Name	Monica Lewis
Sheet	3	of	3	Attorney Docket Number	0756-2294

	7			FOREIGN PATENT DOC	UNIEN 15				
Examiner Inicialy	No.4	Diffice)	Foreign Patent Document Kind Code Number ⁴ (If Invasi		Date of Publication of Gued Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	1		
		JΡ	04-369271	Takeda et al.	12/22/92		FULL		
	1	JР	05-102483	Ucda	04/23/93		FULL		
	1	JP	06-037314	Tsuchimoto	02/10/94		AB		
	i	JР	07-202210	Taniguchi	08/04/95		AB		
		JР	08-078329	Otani	03/22/96		AB		
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		EP	0 451 968	Coxon	10/16/91		ENG		
		JP .	10-247735	Takano et al.	09/14/98		ΑB		
		EP	0 803 911	Adan et al.	10/29/97		ENG		
		JP	10-135468	Yamazaki et al.	05/22/98		AB		
			OTHER PR	ior art – non patent lite	RATURE DOCUMENTS				
Examinet Initials	Cite No.								
	Tanaka, et al., "Ultrafast Operation of V _{th} -Adjused p -n Double-Gate SOI MOSFET'S," IEEE Electron Device Letters, Vol. 15 (Oct., 1994), No. 10, pp. 386-388.								
		Inui et al., "Thresholdless Antiferroelectricity in Liquid Crystals and its Application to Displays," J. Mater Chem., 1996, 6(4), pp. 671-673.							
		Takada, et al., PROCEEDINGS OF 46 TH SPRING MEETINGS OF THE SOCIETY FOR APPLIED PHYSICS OF JAPAN, 28P-V-8, page 1316.							
·· -		Hw		Polysilicon/TiN Stacked-0		ly-Depleted	ENG		
		Yoshibara, EKISHO, Vol. 3, No. 3, pp. 190-194.							
	Furue et al., "P-78: Characteristics and Driving Scheme of Polymer-Stabilized Monostable FLCD Exhibiting Fast Response Time and High Contrast Ratio with Gray- Scale Capability," SID '98 DIGEST, pp. 782-785.								
		Hat	ano et al, "A Nove iability and Perform	l Self-Aligned Gate-Over	lapped LDD Poly-Si	TFT with High	ENG		

Examiner	Date	
Signature	Considered	

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